

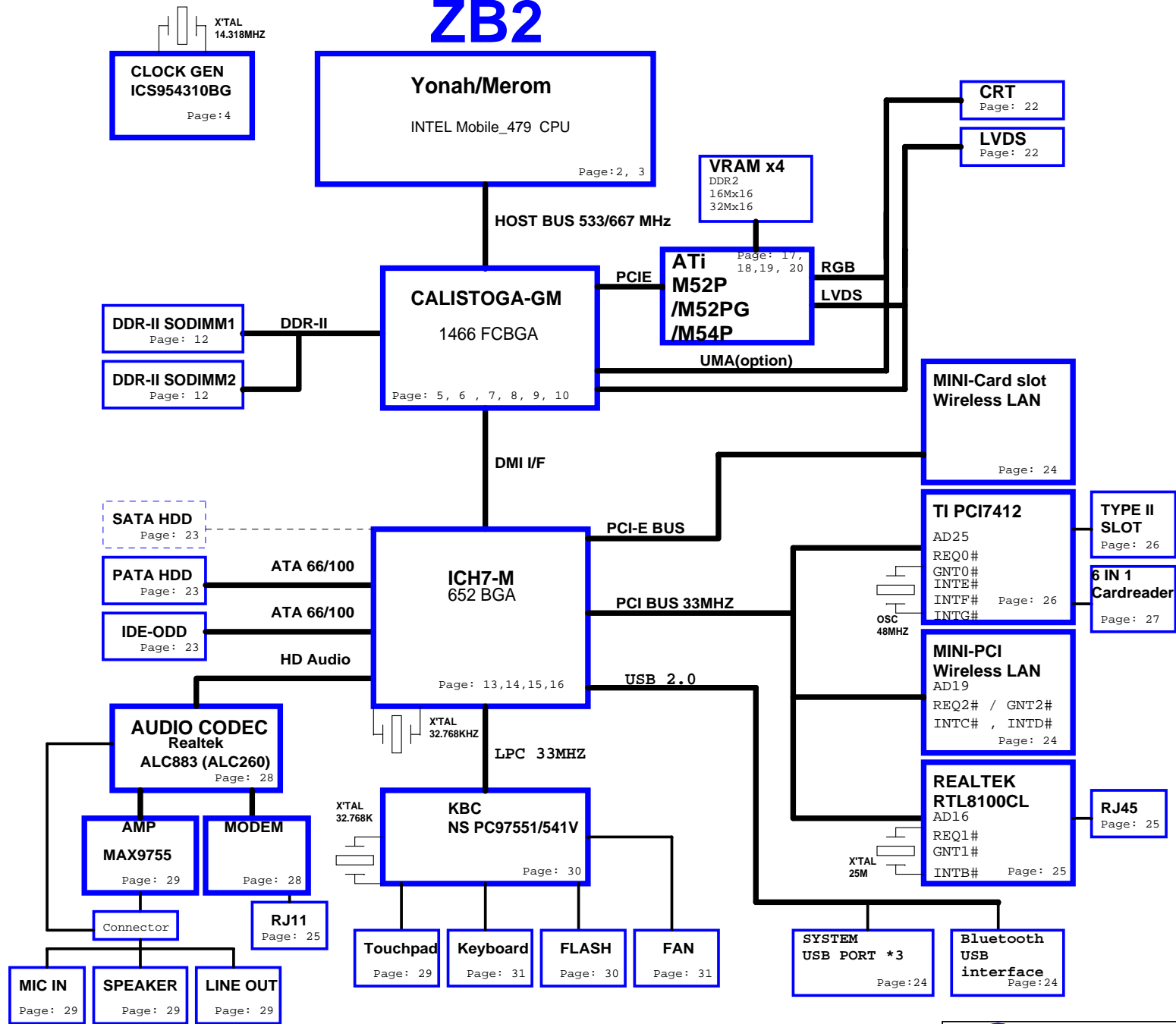
CPU CORE
SENTECH
SC451ITSTR
Page: 32

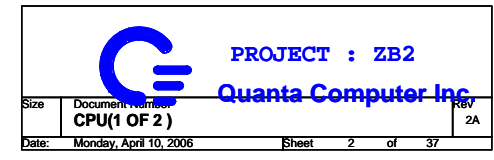
SYSTEM 3V/5V
MAXIM
MAX1999
+3VPCU
+3V S5
+3VSUS
+3V
+5VPCU
+5VSUS
+5V
+15V
Page: 33

+1.8VSUS
+1.8V
+0.9VSUS
+0.9V
+1.5V
+1.05V
Page: 34

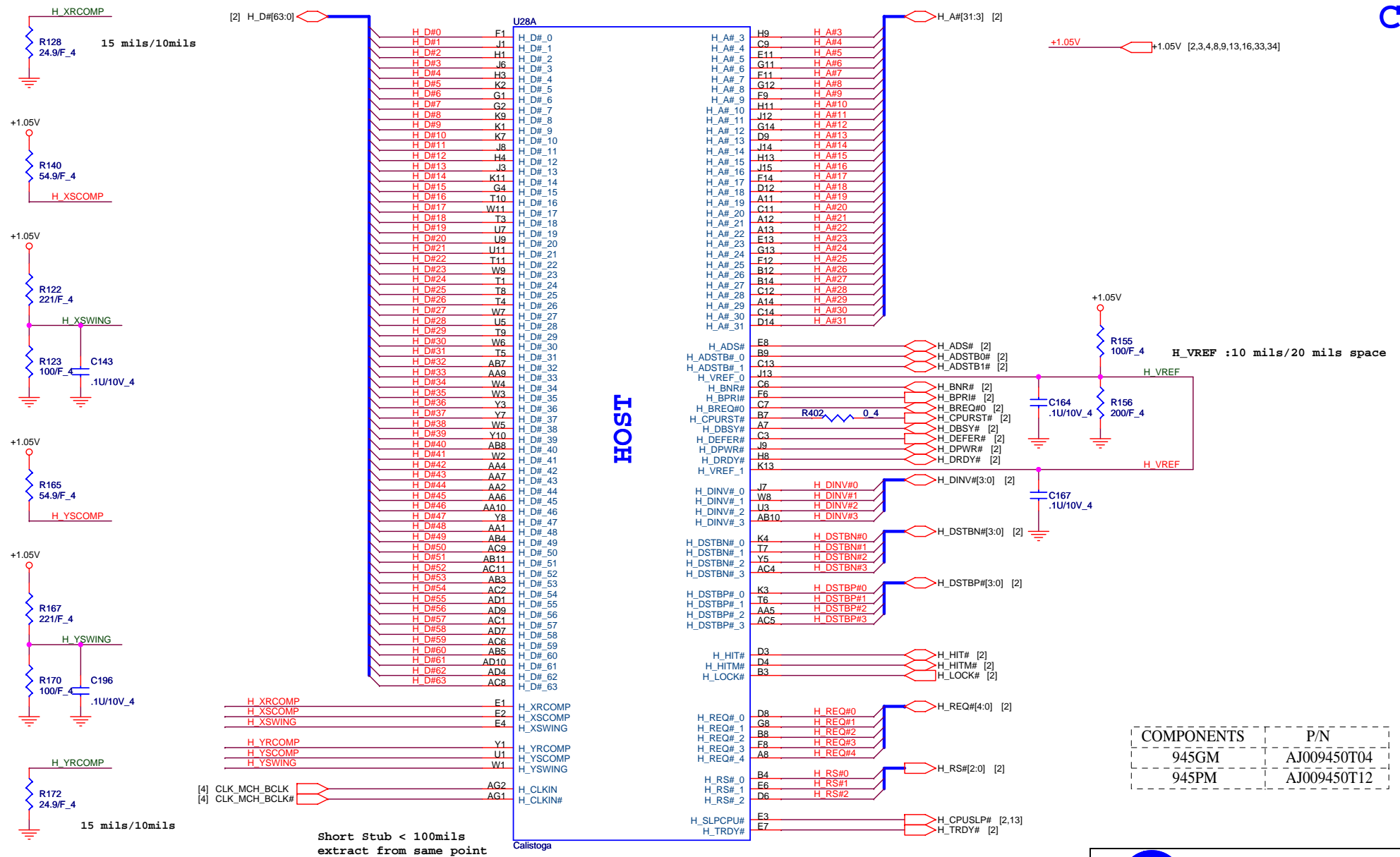
+2.5V
+1.2V
VGA_CORE
Page: 36

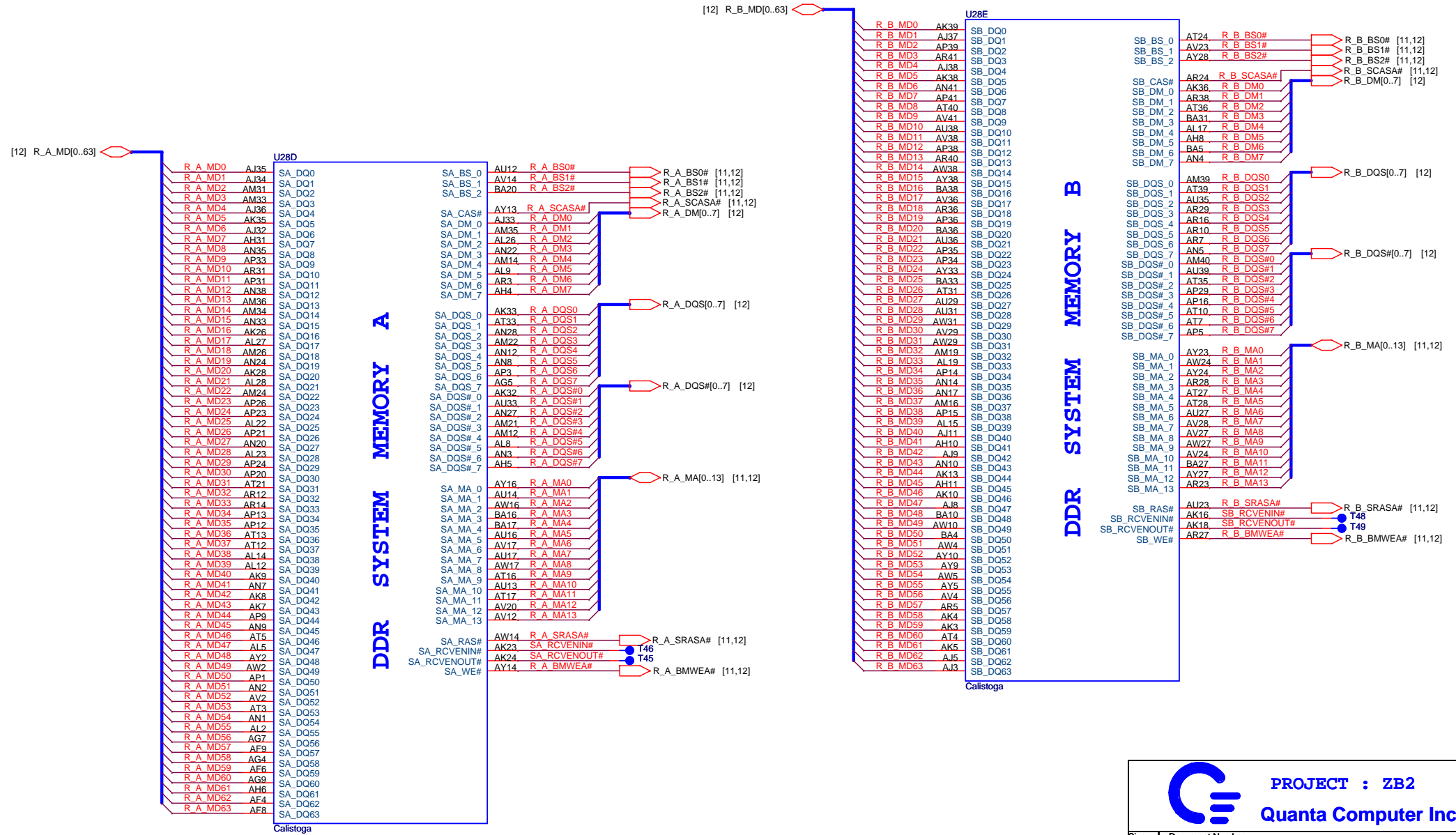
BATTERY CHARGER
MAXIM
MAX8724
Page: 35







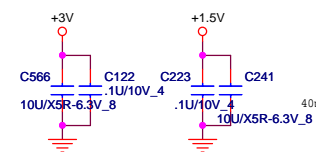
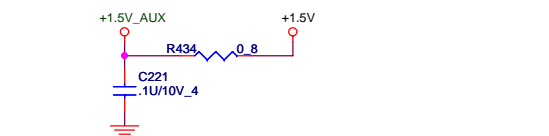
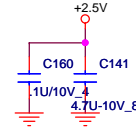




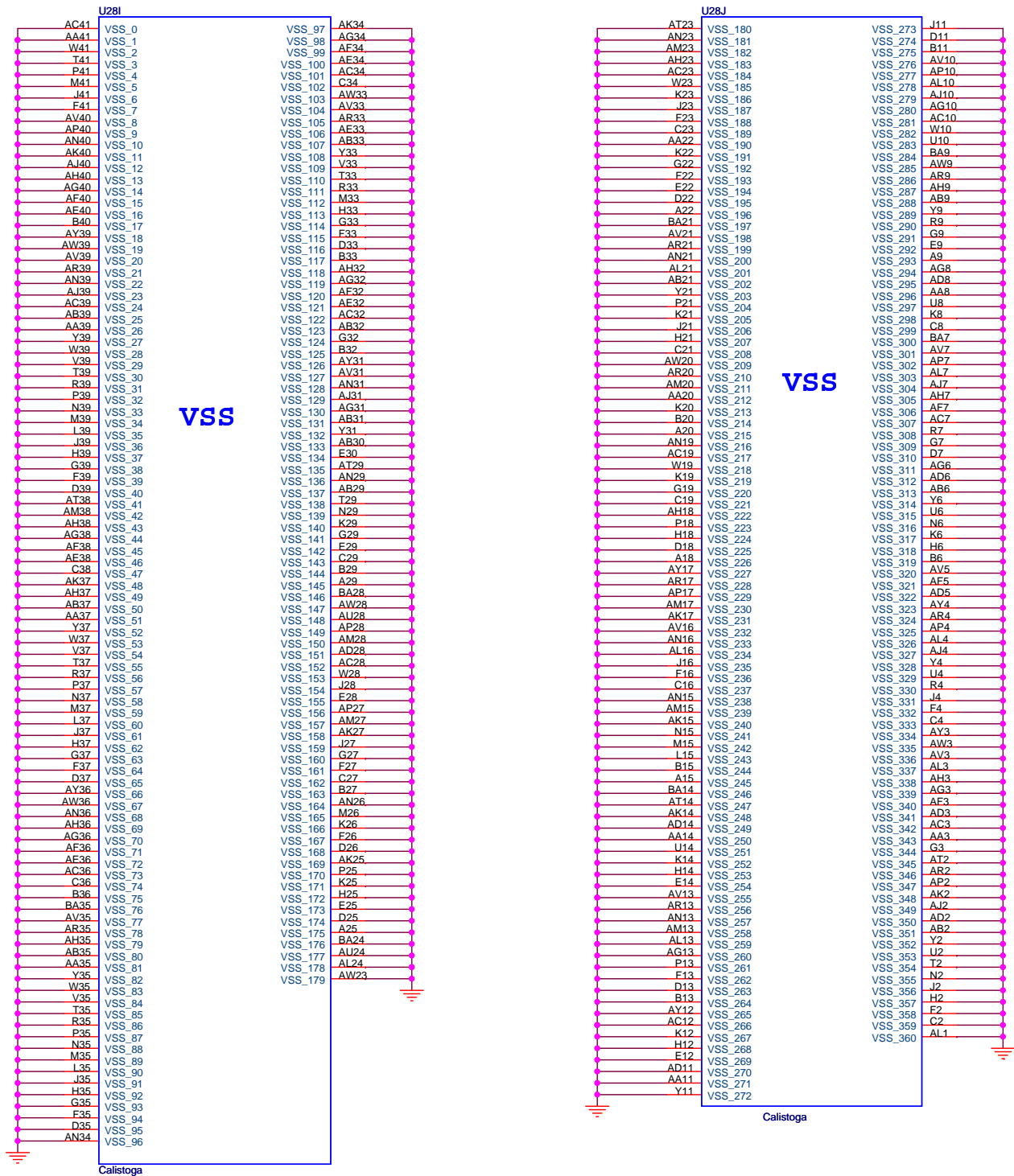
PROJECT : ZB2
Quanta Computer Inc.

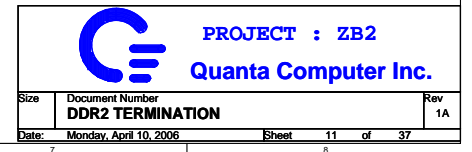
Size	Document Number	Rev
	GMCH DDR(2 OF 6)	1A
Date:	Monday, April 10, 2006	Sheet 6 of 37





+1.05V		+1.05V [2,3,4,5,8,13,16,33,34]
+1.5V		+1.5V [3,7,14,16,24,28,33,34]
+V1.5_PCIE		+V1.5_PCIE [7]
+2.5V		+2.5V [17,19,33,36]
+3V		+3V [2,4,7,12,13,14,15,16,17,19,20,22,23,24,25,26,27,28,29,30,31,32,33,34,36]

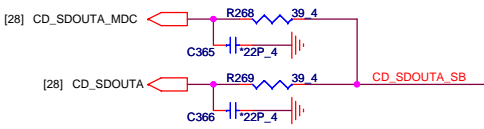
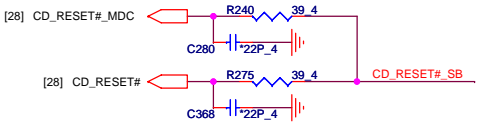






Circuit diagram of a crystal oscillator. A 32.768KHZ crystal (Y5) is connected between two 15P-50V capacitors (C643 and C651). The other ends of the capacitors are connected to ground. The crystal is also connected to a 10M_6 resistor (R470) which is connected to the CLK_32KX1 and CLK_32KX2 signals.

The diagram shows two signal lines, CD_BITCLKA_MDC [28] and CD_BITCLKA [28], each connected to a 39.4 ohm resistor (R267 and R469 respectively) and a 22pF capacitor (C354 and C649 respectively) to ground.



VCCRTC

R251
332K/F_4

R253
332K

CLK_32KX1
CLK_32KX2

AB1
AB2

RTCRST#

AA3

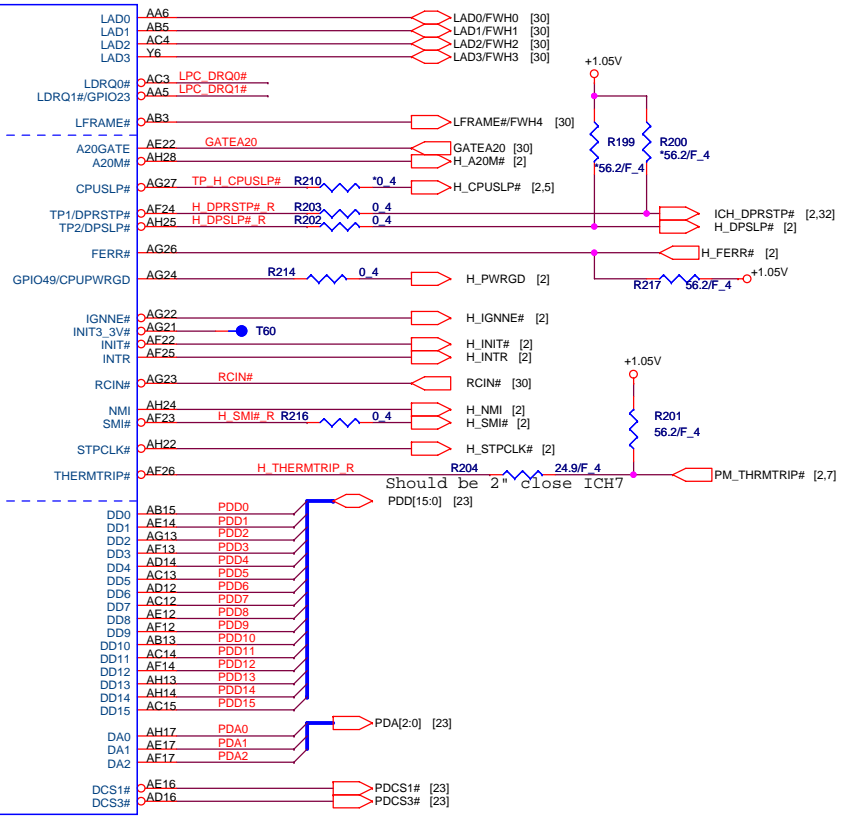
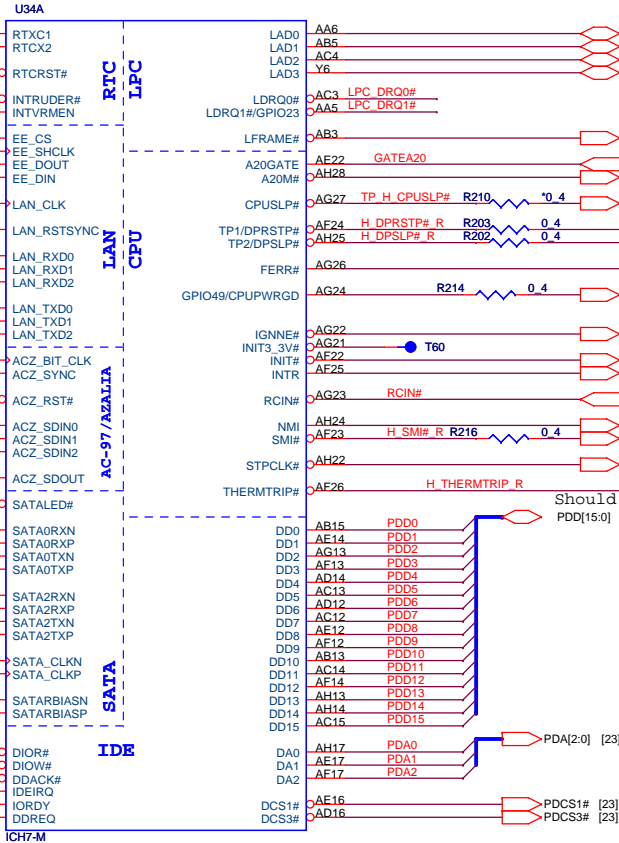
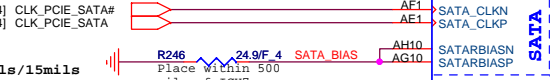
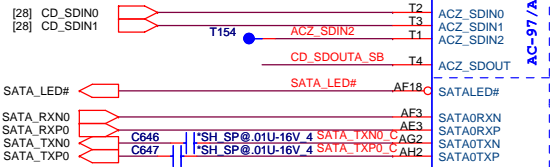
SM_INTRUDER#

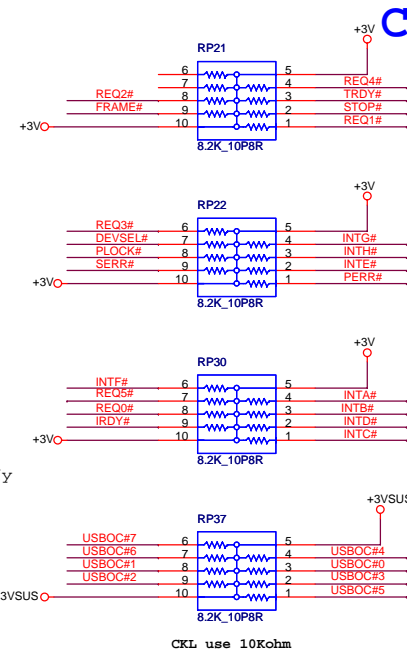
Y5

ICH_INTVRMEN

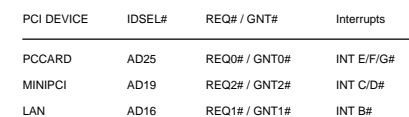
W4

W1
Y1
Y2
W3
V3

[illegible]

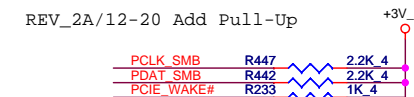
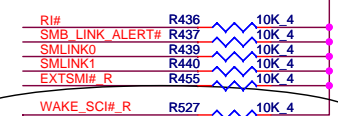
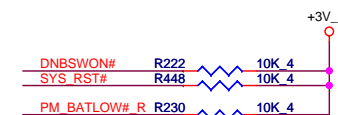
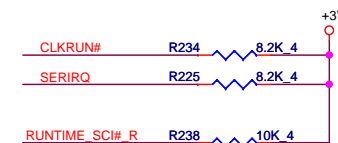
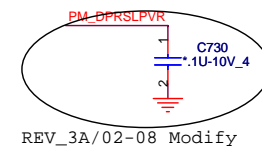


	STRAP	GNT5# R1	GNT4# R2
LPC (default)	11	UNSTUFF	UNSTUFF
PCI	10	UNSTUFF	STUFF
SPI	01	STUFF	UNSTUFF

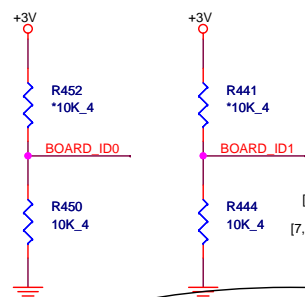
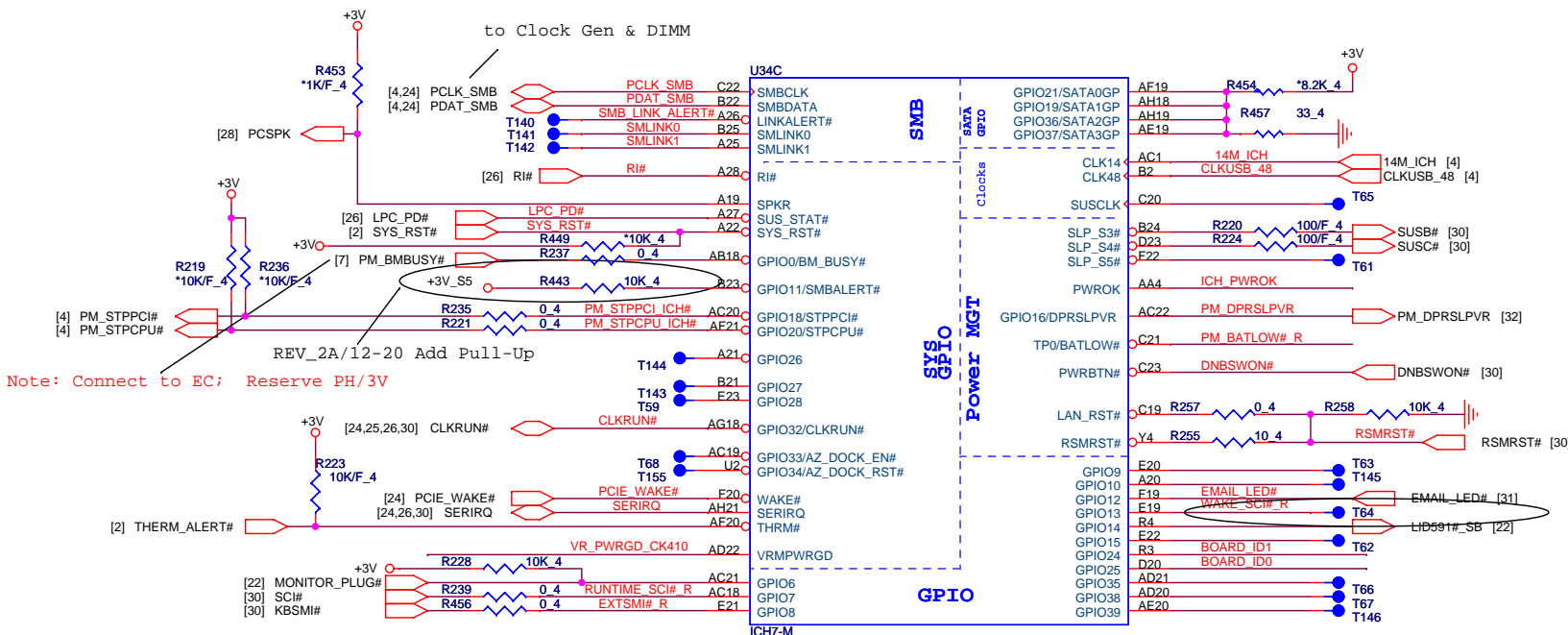


PROJECT : ZB2
Quanta Computer Inc.

Size	Document Number ICH7-M PCI E (2 OF 4)	Rev 3A
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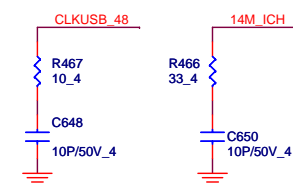
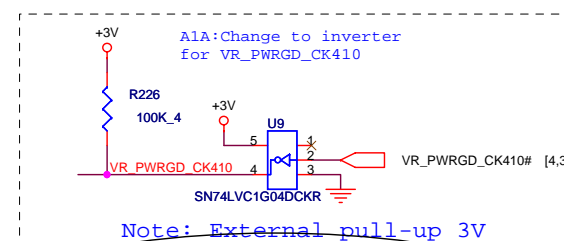
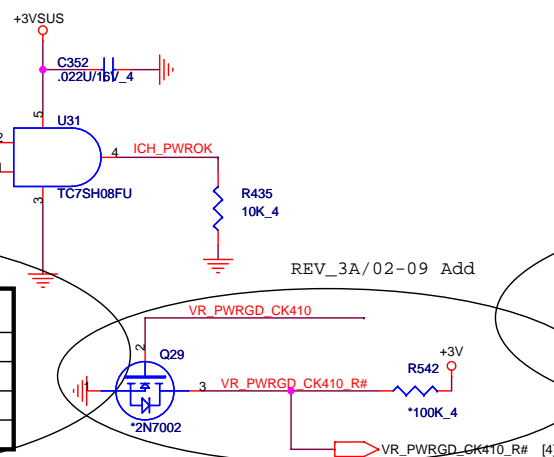


GPI025 /Suspend rail is a HW strap , don't pull down .



REV_3C/03-29
Modify

Board ID	CardReader	CCD
ID0 , ID1 00	X	X
ID0 , ID1 01	X	V
ID0 , ID1 10	V	X
ID0 , ID1 11	V	V

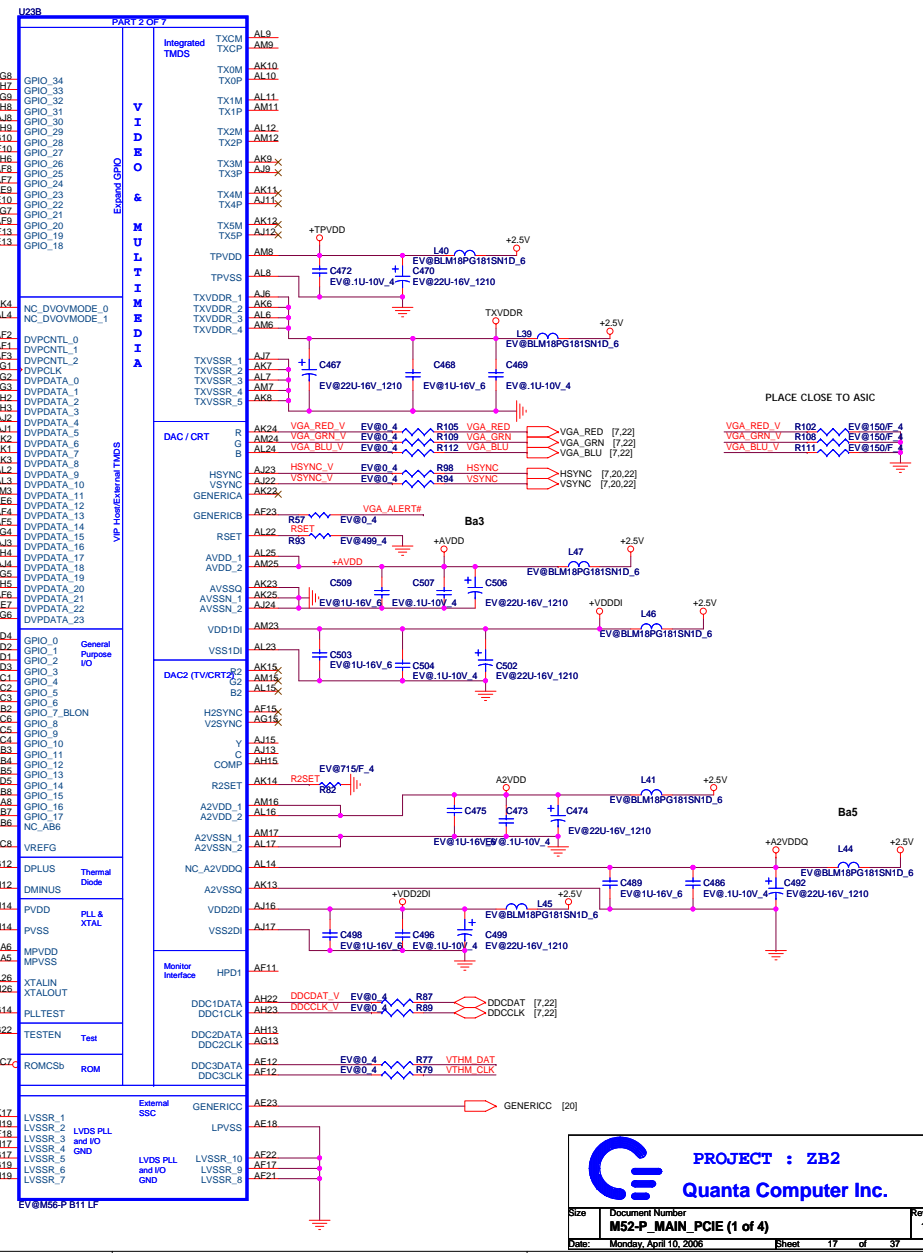
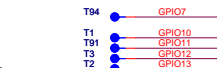
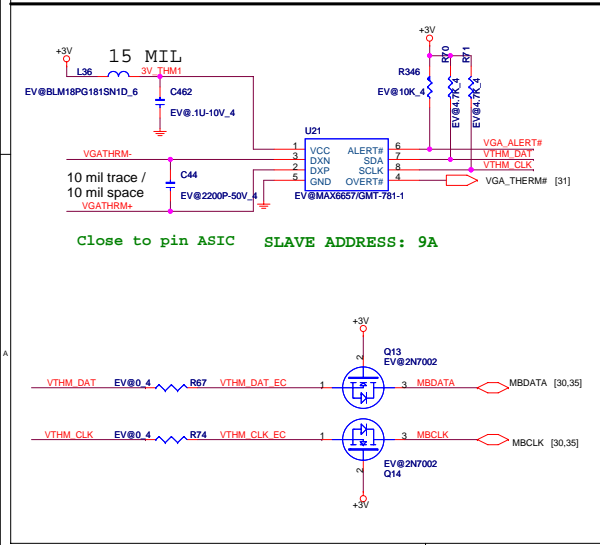
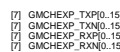


PROJECT : ZB2
Quanta Computer Inc.

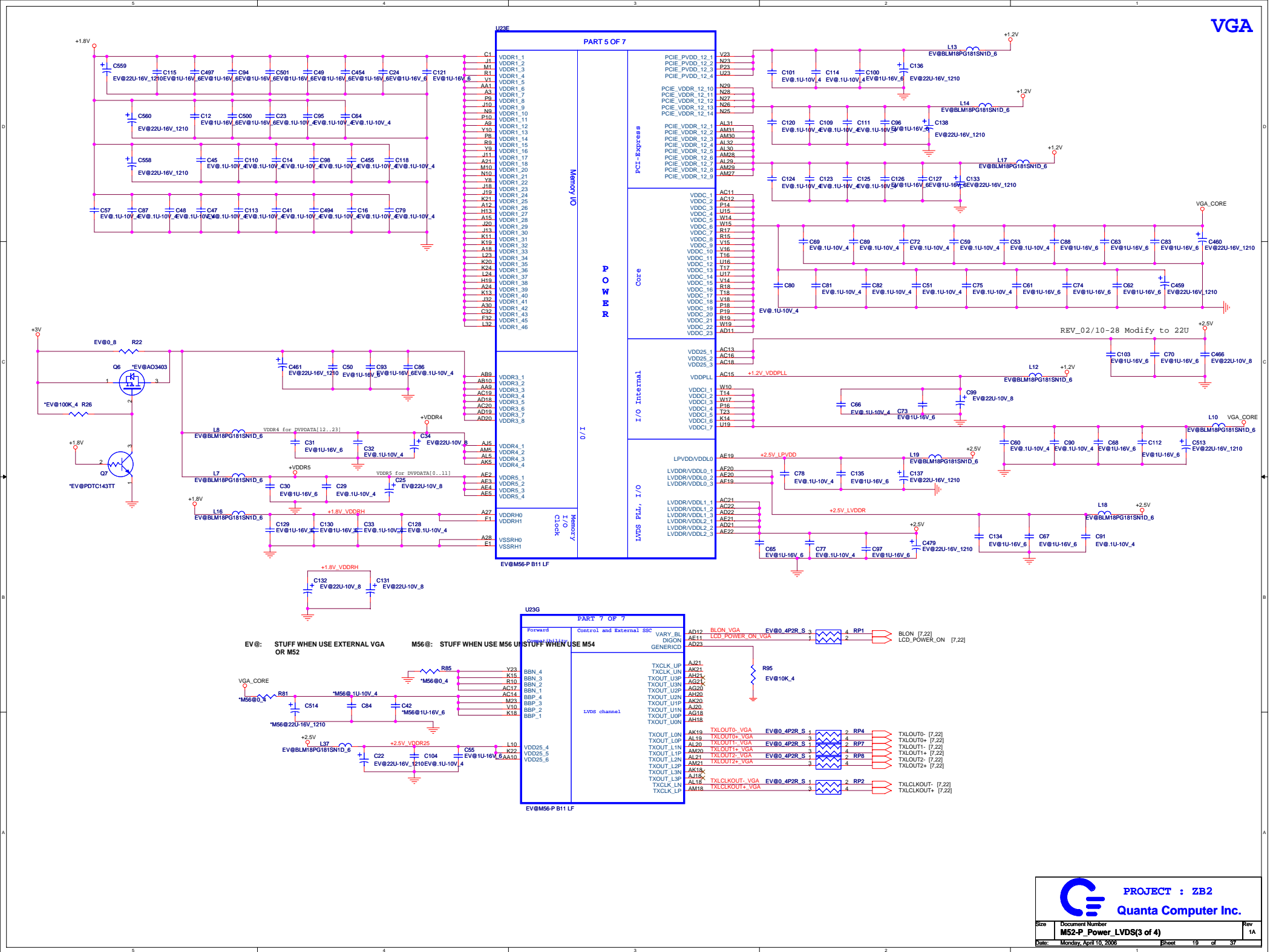
Size	Document Number	Rev
	ICH7-M GPIO (3 OF 4)	3C
Date:	Monday, April 10, 2006	Sheet 15 of 37

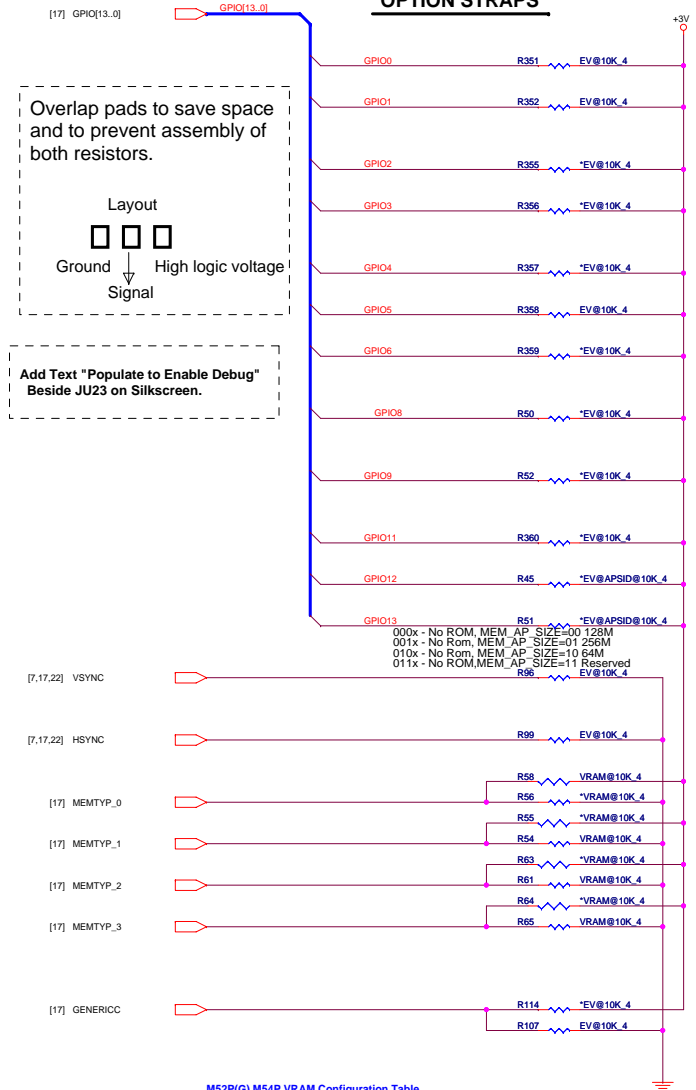
NOTE: some of the PCIE testpoints will be available through via on traces.

```
[7] GMCHEXP_TXP[0..15]
[7] GMCHEXP_TXN[0..15]
[7] GMCHEXP_RXP[0..15]
[7] GMCHEXP_RXN[0..15]
```



VGA

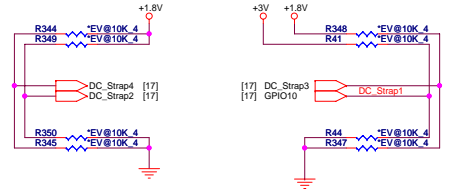




M52P(G) M54P VRAM Configuration Table

MEMTYP_ [3:0]	DESCRIPTION	Size
0000	DDR2, 16Mx16, 2pcs	64M
0001	DDR2, 16Mx16, 4pcs	128M
0010	DDR2, 32Mx16, 2pcs	128M
0011	DDR2, 32Mx16, 4pcs	256M
others	Reserved	

Default



M56-P Strap

STRAPS	PIN	DESCRIPTION	Board DEFAULT
TX_PWRS_ENB	GPIO0 (Internal pull-down)	Transmitter Power Saving Enable 0: 50% Tx output swing 1.full Tx output swing	1
TX_DEEMPH_EN	GPIO1 (Internal pull-down)	Transmitter De-emphasis Enable 0: Tx de-emphasis disabled 1.Tx de-emphasis enabled	1
	GPIO(3:2) (Internal pull-down)	RSVD	
DEBUG_ACCESS	GPIO4 (Internal pull-down)	Strap to set the debug muxes to bring out DEBUG signals even if registers are inaccessible	0
	GPIO5 (Internal pull-down)	RSVD	
	GPIO6 (Internal pull-down)	RSVD	
Force_Compilance	GPIO8 (Internal pull-down)	Force chip to get to compliance state quickly for Tester purposes	0
ROMIDCFG(3:0)	GPIO(9,13,12,11) (Internal pull-down)	If no ROM attached, controls chip IDis. If rom attached identifies ROM type 000x - No ROM, MEM_AP_SIZE=00 128M 001x - No Rom, MEM_AP_SIZE=01 256M 010x - No Rom, MEM_AP_SIZE=10 64M 011x - No ROM, MEM_AP_SIZE=11 Reserved 1000 - Parallel ROM, chip IDis from ROM 1001 - Serial AT25F1024 ROM (Atmel), chip IDis from ROM 1010 - Serial AT45DB011 ROM (Atmel), chip IDis from ROM 1011 - Serial M25P10 ROM (ST), chip IDis from ROM 1100 - Serial M25P05 ROM (ST), chip IDis from ROM 1100 - Serial NX25F011B ROM (ISSI), chip IDis from ROM	000
VIP_DEVICE	VSYNC	Indicates if any slave VIP host devices drove this pin low during reset. 0- Slave VIP host port device present. 1-No slave VIP port devices reporting presence during reset	No default
	H2SYNC, V2SYNC, SENERICC	RSVD	
	VSYNC	RSVD	
	HSYNC	RSVD	
	PCIE_TEST	RSVD	


Memory Aperture Size Select
When no ROM is attached, GPIO_9 is set to 0
GPIO_[13:12] is used to select the memory aperture size.
GPIO_[13:12] = 00: 128M memory aperture, same as ROM strap 00
GPIO_[13:12] = 01: 256M memory aperture, same as ROM strap 01
GPIO_[13:12] = 10: 64M memory aperture, same as ROM strap 10
GPIO_[13:12] = 11: reserved, same as ROM strap 11

Default: 128M memory aperture.
GPIO_[13:12] = 01 (256M memory aperture) recommended for designs with 256MB or 512MB of physical memory.

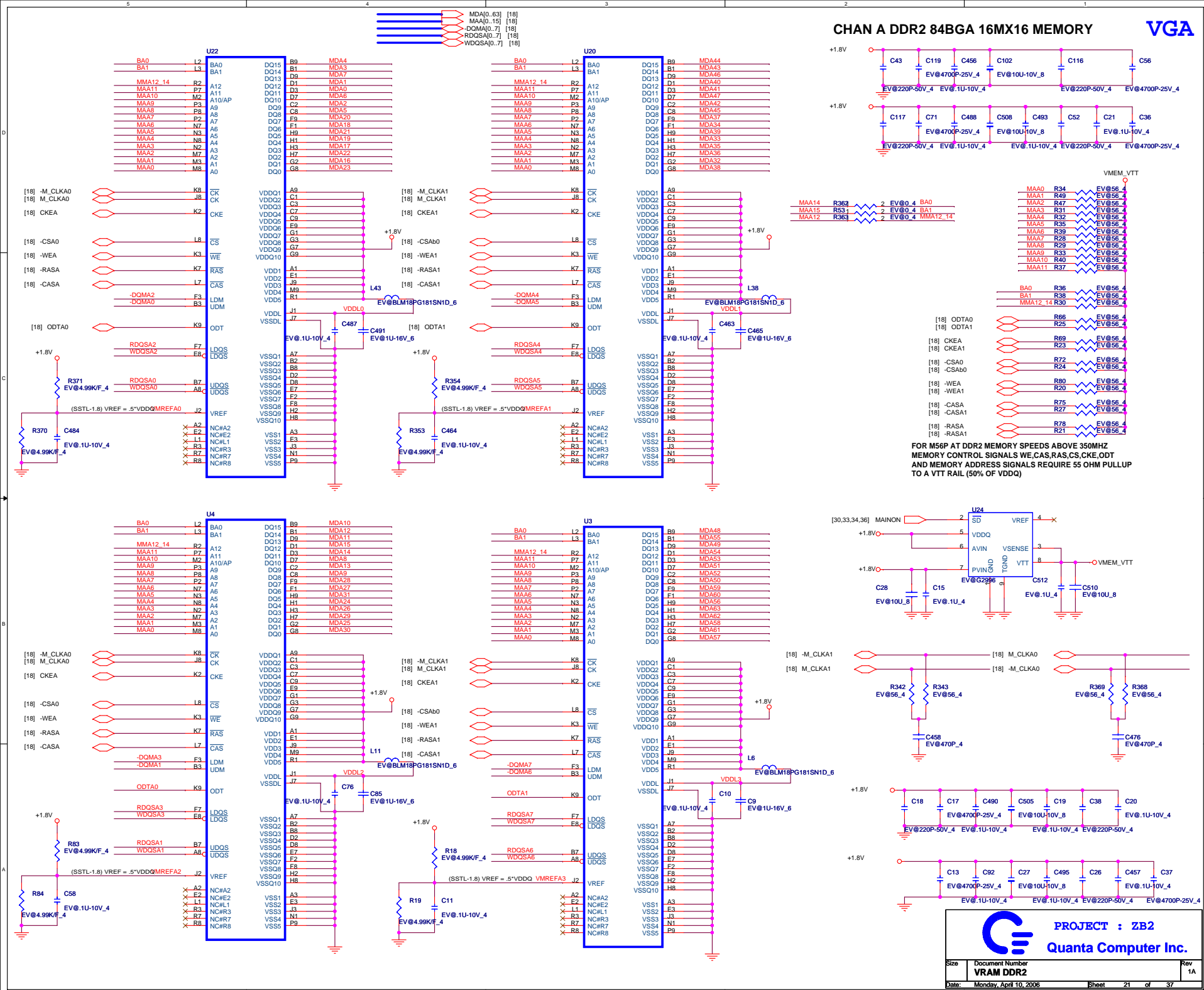
Board Straps

REV. 0.3

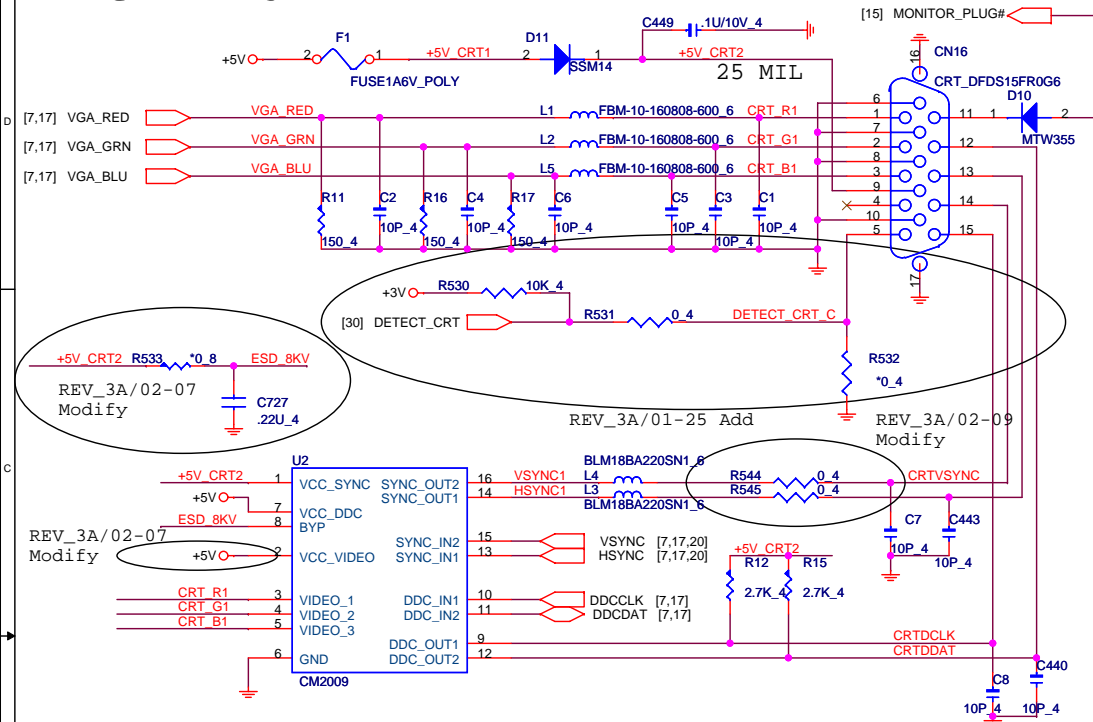
STRAPS	PIN	DESCRIPTION	VALUE
MEMTYPE(1:0)	GPIO25,26	Memory identification for BIOS 00 - DDR2 16X16X4pcs 128MB 01 - DDR2 32X16X4pcs 256MB 10 - DDR2 16X16X2pcs 64MB 11 - DDR2 32X16X2pcs 128MB	
DC_Strip1	GPIO(10)	Internal TMDIS Enabled 0 - Disabled 1 - Enabled	1
DC_Strip2	LCDDATA(13)	Video Capture Enabled 0 - Disabled 1 - Not detected	0
DC_Strip3	LCDDATA(14)	HDTV out detect 0 - Detected 1 - Enabled	1
DC_Strip4, DEMUX_SEL	LCDDATA(15,19)	Video capture enable 00 - DAC2 Off 01 - DAC2 On as CRT 10 - DAC2 On as TVOUT 11 - DAC2 On as TVOUT and CRT	01
PAL/NTSC	LCDDATA(18)	TVO Standard Default (Resistor pull-up and switch short to GND) 0 - PAL (on board resistor pull-down and switch closed) 1 - NTSC (on board resistor pull-up)	1

PROJECT : ZB2
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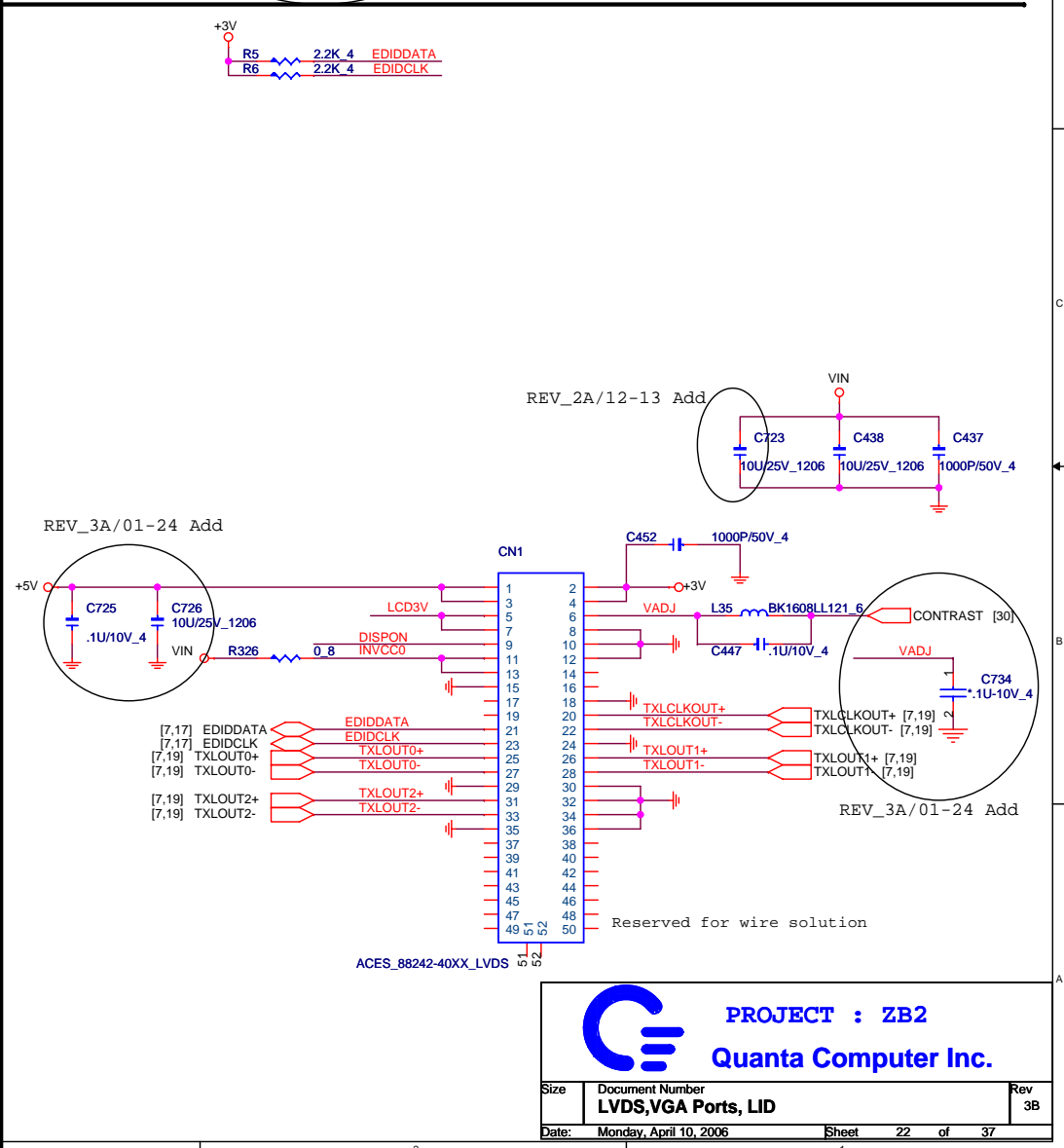
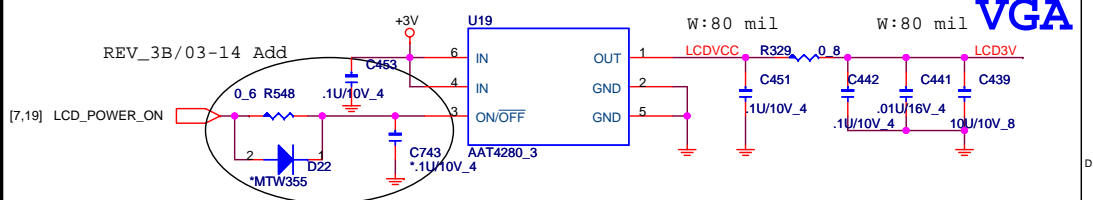
Size
Document Number
M52-P_Straps (4 of 4)
Date: Monday, April 10, 2006
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Rev 1A



CRT PORT



VGA



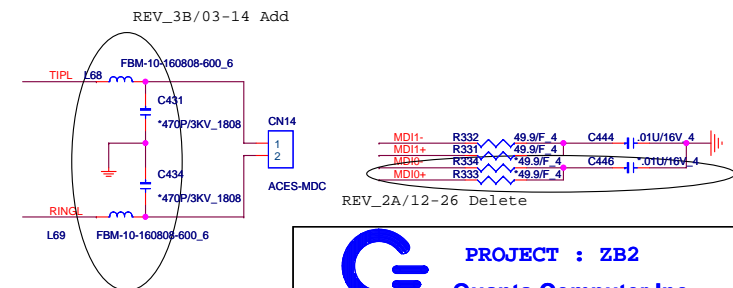
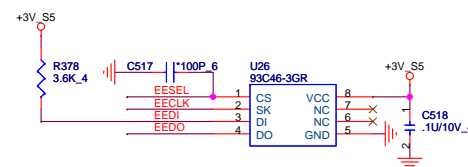
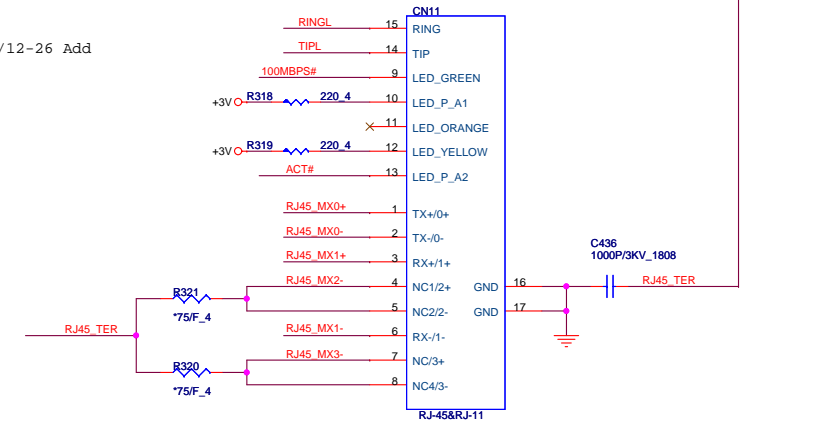
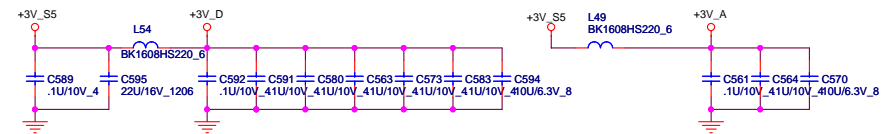
PROJECT : ZB2
Quanta Computer Inc.

Size	Document Number	Rev
	LVDs,VGA Ports, LID	3B
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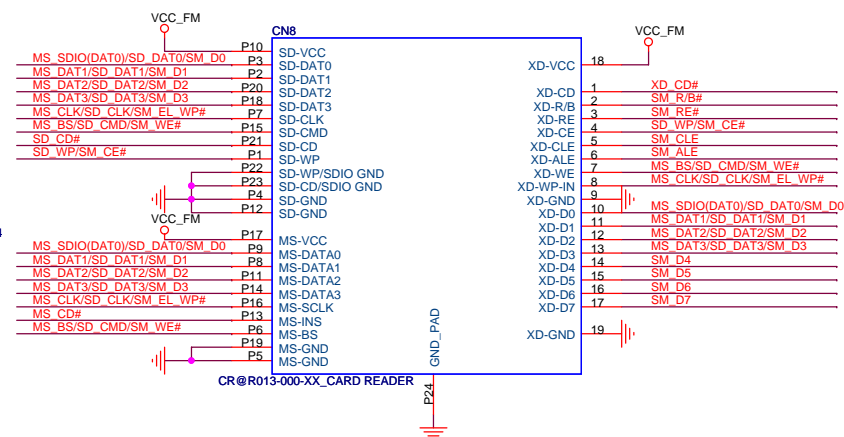
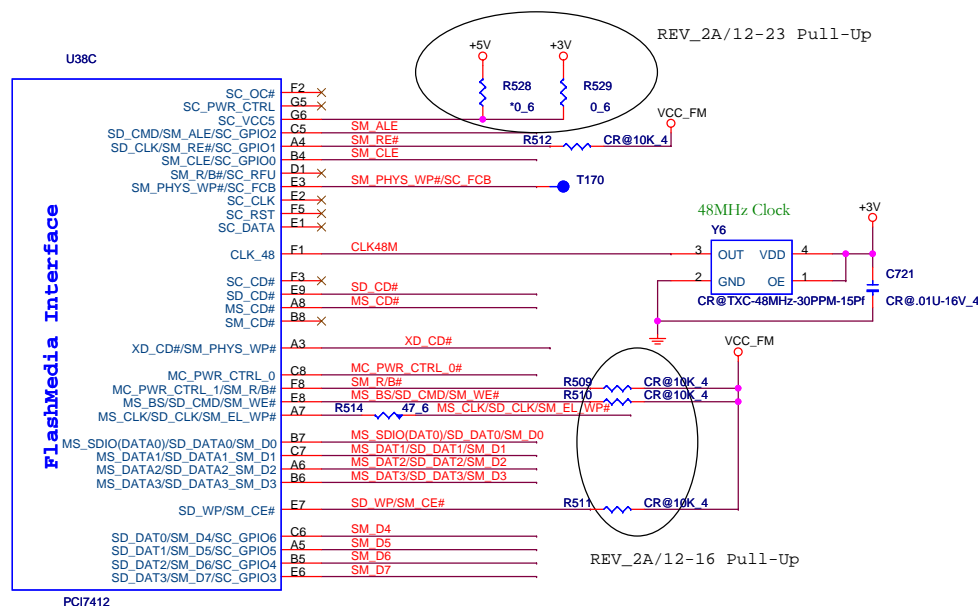


LAN

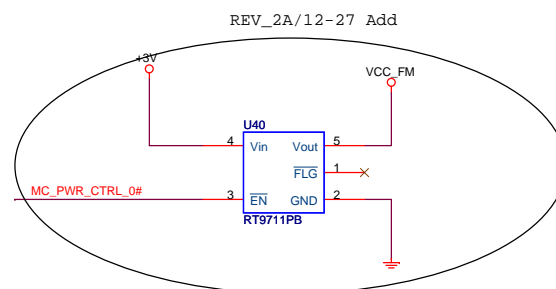
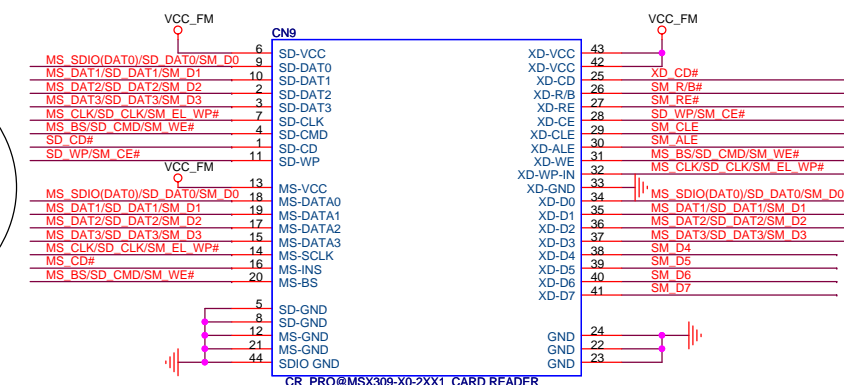
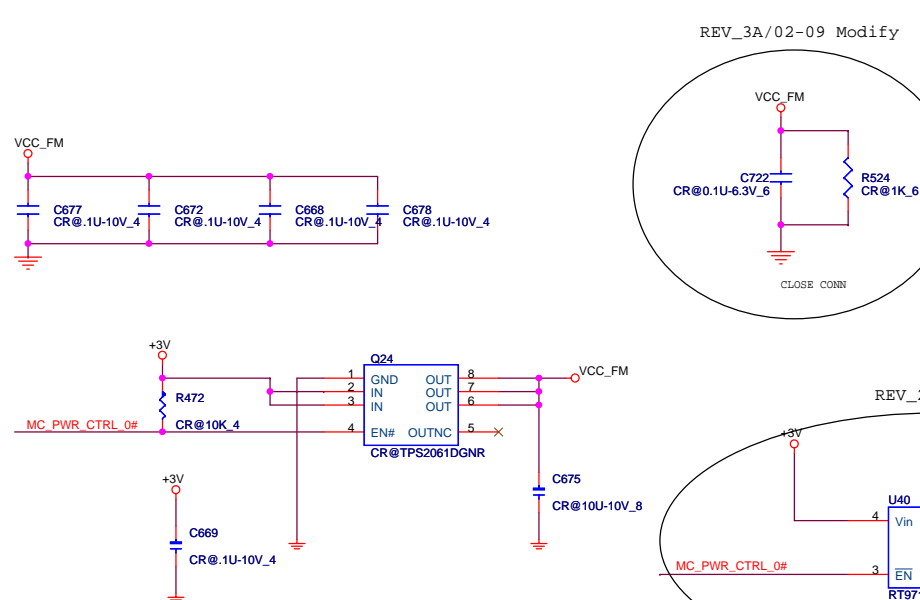




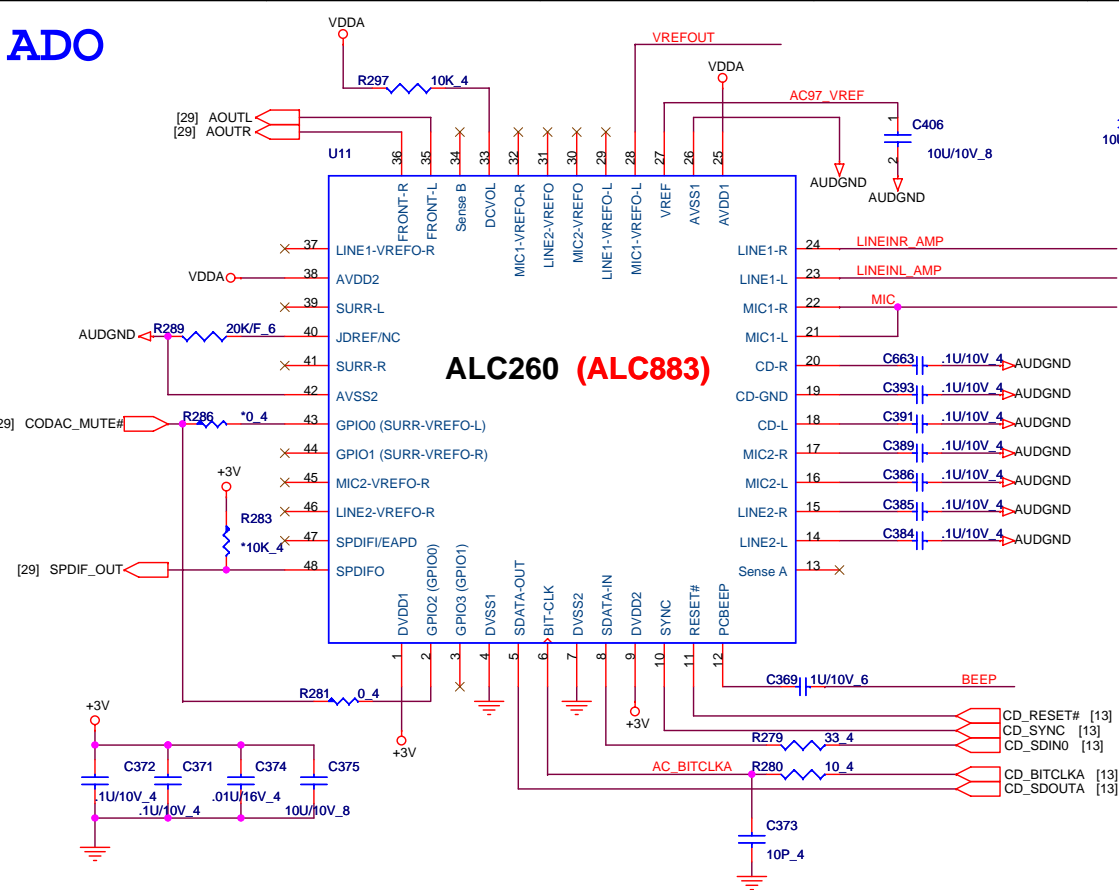
DO NOT INSERT SD/MMC, MEMORYSTICK AND XD SIMULTANEOUSLY.



5 IN1 CARD READER

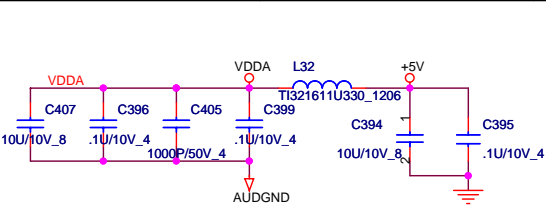
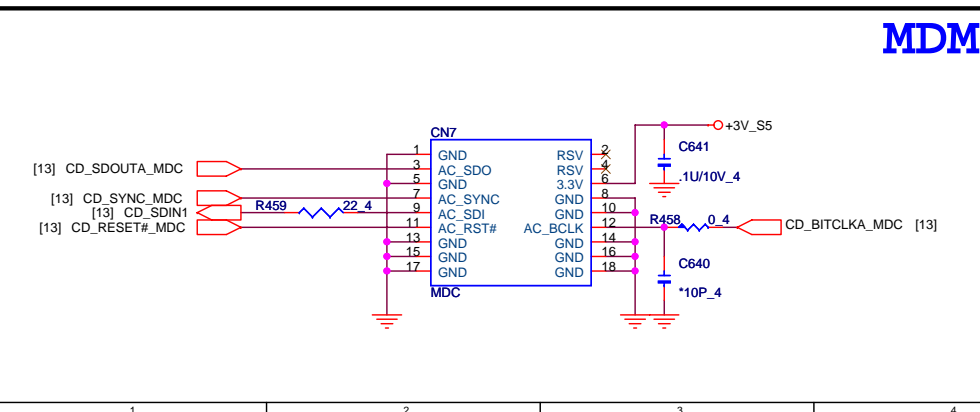


ADO

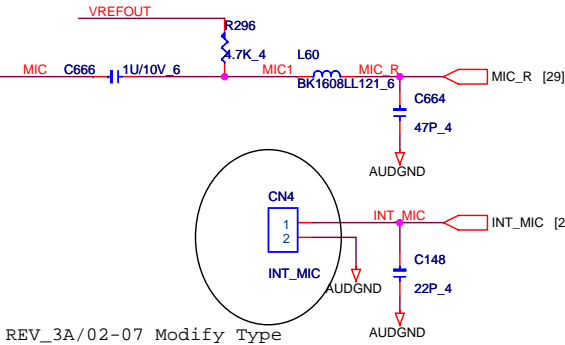
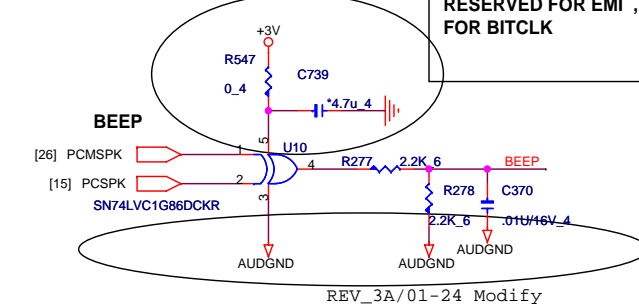


ALC260 (ALC883)

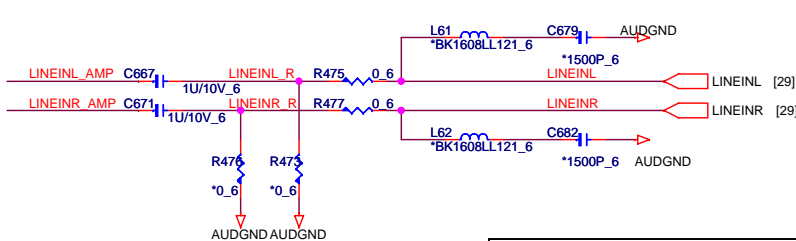
MDM



REV_3B/03-10 Modify



REV_3A/02-07 Modify Type



RESERVED FOR EMI ,
UNDER AUDIO JACK

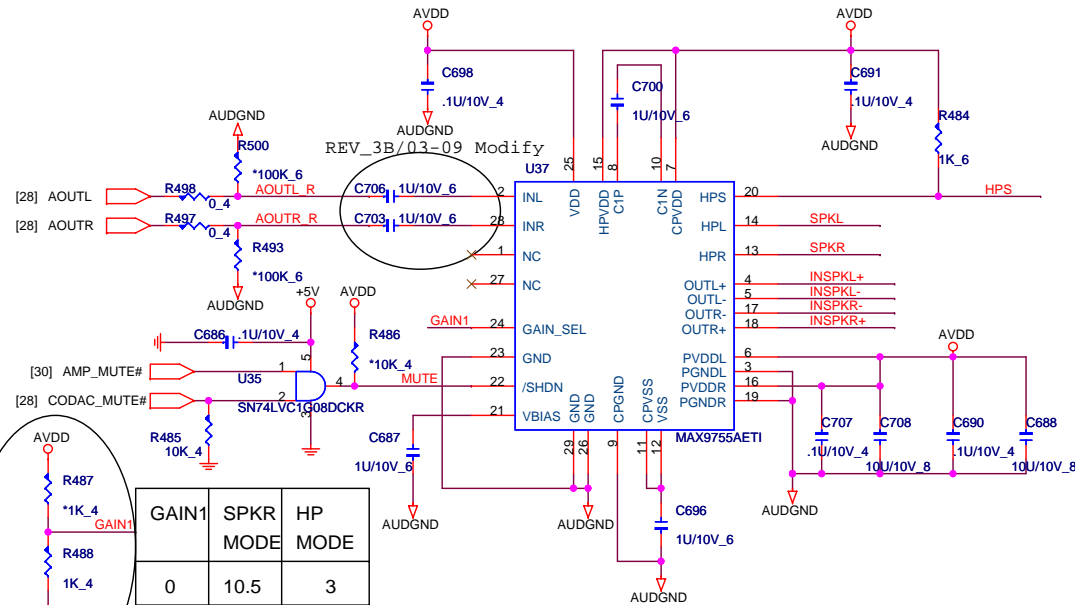
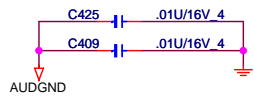
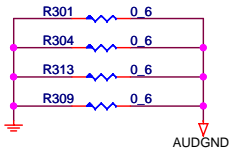
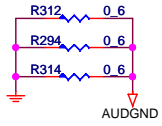
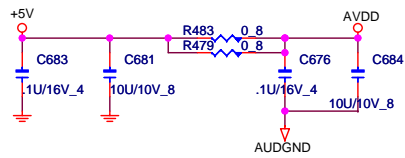
RESERVED FOR EMI ,
FOR BITCLK

PROJECT : ZB2
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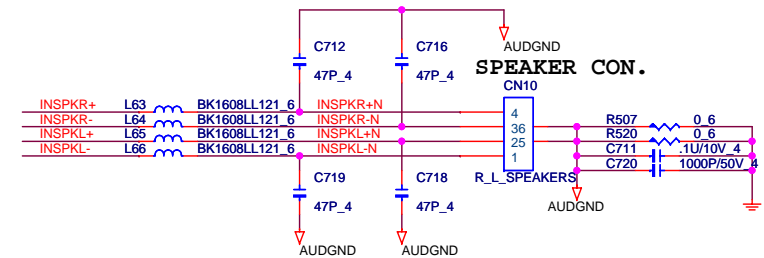
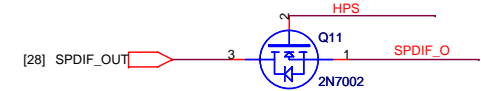
Size	Document Number	Rev
	REALTEK ALC883	3B
Date:	Monday, April 10, 2006	Sheet 28 of 37



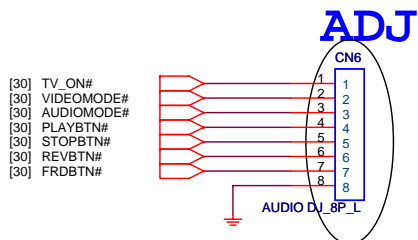
PROJECT : ZB2
Quanta Computer Inc.



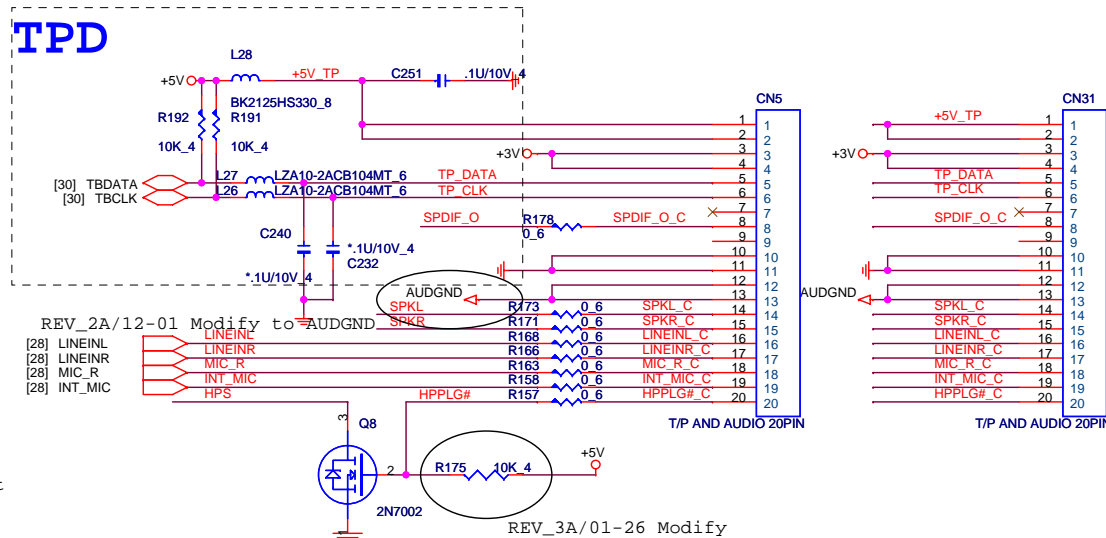
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



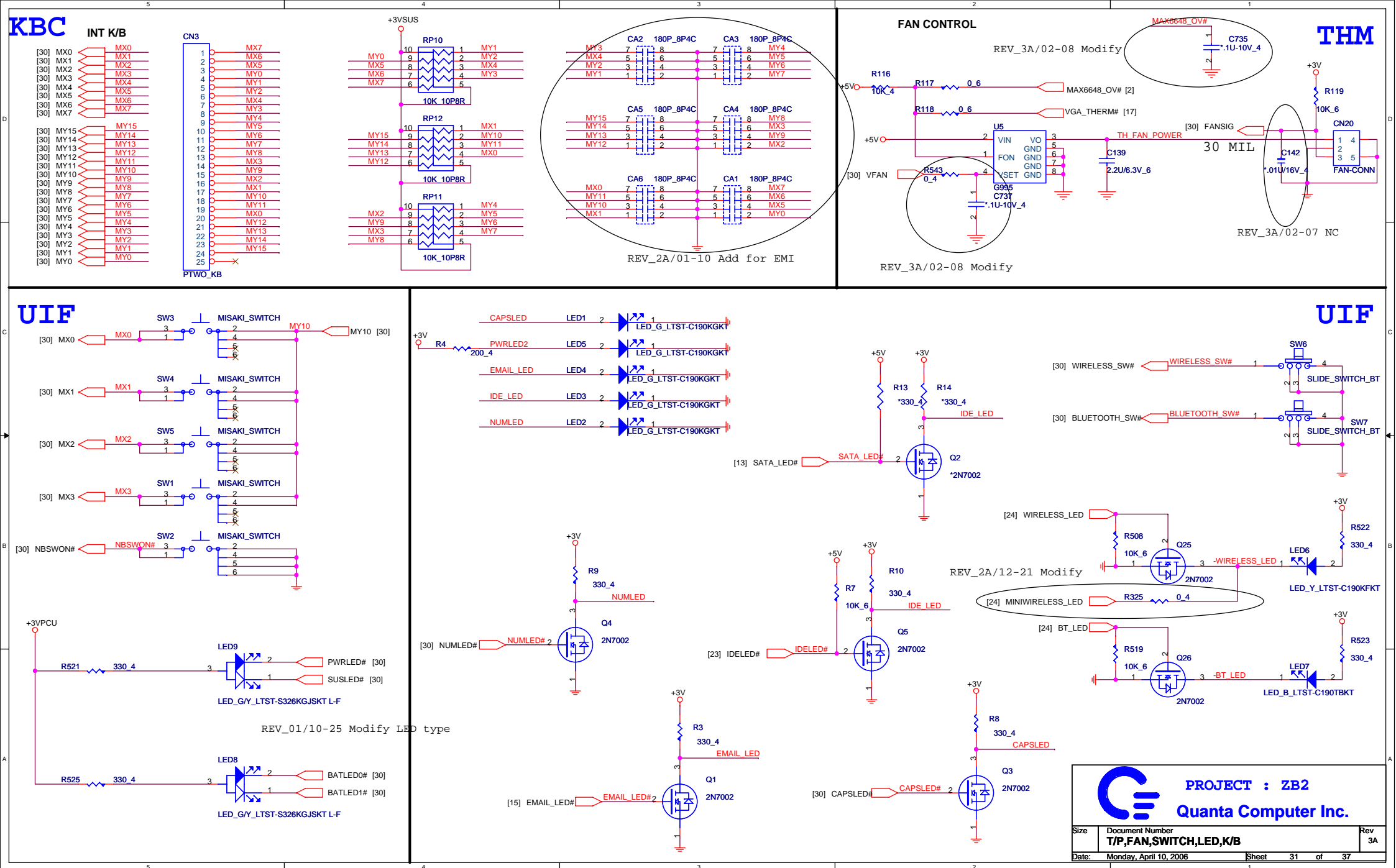
AUDIO DJ

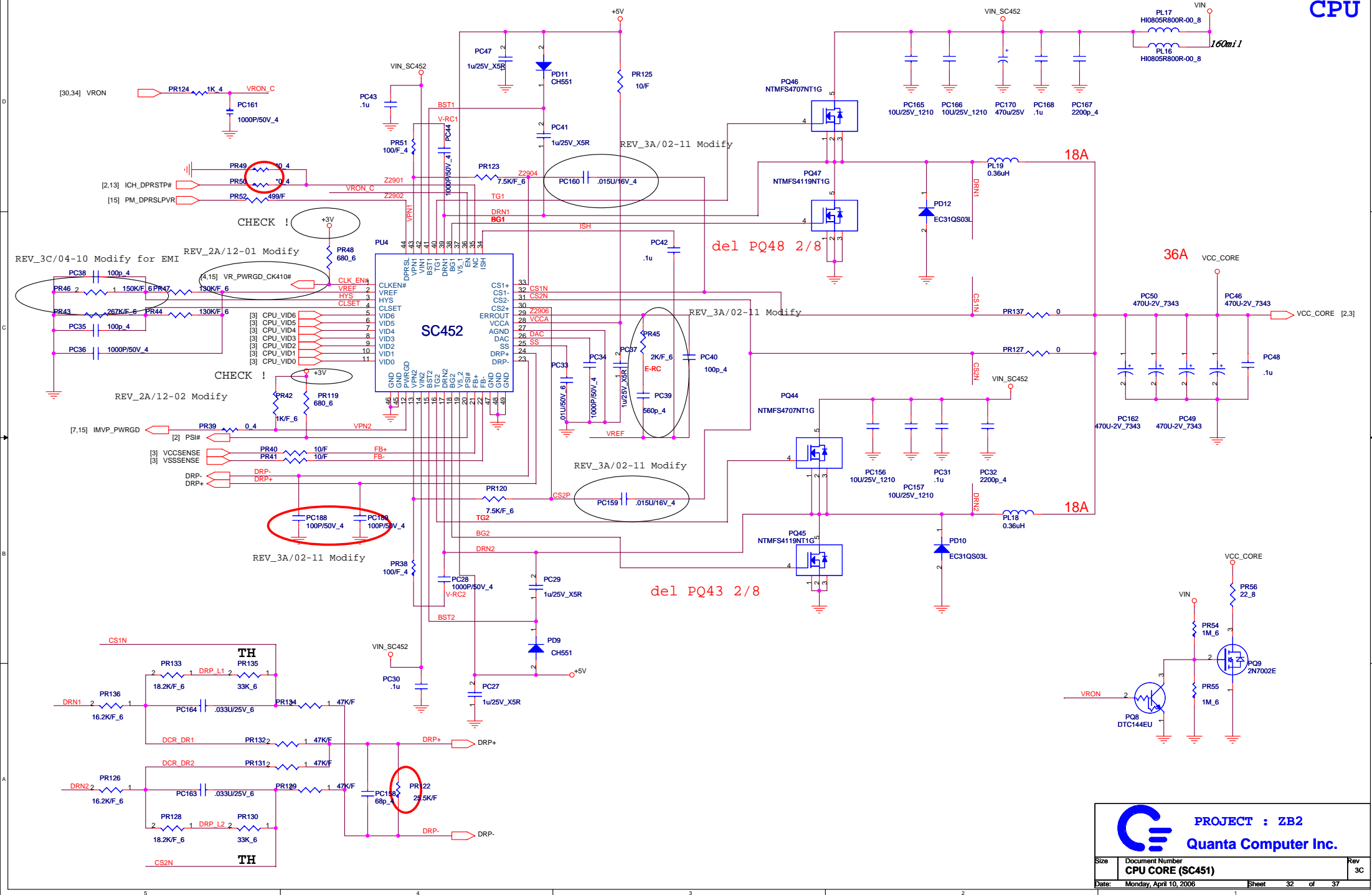


REV_3B/03-09 Modify Footprint









DCD

```
AO4916 Rds on = 27mOhm
ILOAD * Rds on * 10 = ILIM
ILIM5 = 1.485V Current limit 5.5A
ILIM3 = 1.35V Current limit 5A
```

